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Serial No.  
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New Application

LIST OF RELATED ART CITED BY APPLICANT  
(use several sheets if necessary)

Applicant(s):  
Thomas STURM

Filing Date  
Herewith

Group Art Unit  
Unknown

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
DL	AA	5,574,751	November 12, 1996	Trelewicz			
DL	AB	5,566,191	October 15, 1996	Ohnishi et al			
DL	AC	5,721,746	February 24, 1998	Hladik et al			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
DL	AJ	EP 0 391 354 A2	10 October 1990	Europe				
DL	AK	DE 44 37 984 A1	14 August 1996	Germany				
DL	AL	DE 40 38 251 A1	04 June 1992	Germany				
	AM							
	AN							
	AO							
	AP							

OTHER RELATED ART (Including Author, Title, Date, Pertinent Pages, Etc.)

DL	AQ	JOERESSEN, Olaf J, et al., "HIGH-SPEED VLSI ARCHITECTURES FOR SOFT-OUTPUT VITERBI DECODING", 8367 Journal of VLSI Signal Processing (1994), No. 2, Dordrecht, NL, pages 169-180					
	AR						
	AS						
	AT						
	AU						
	AV						
	AW						
	AX						
	AY						

Examiner *David Lugo*

Date Considered 11/24/04

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.